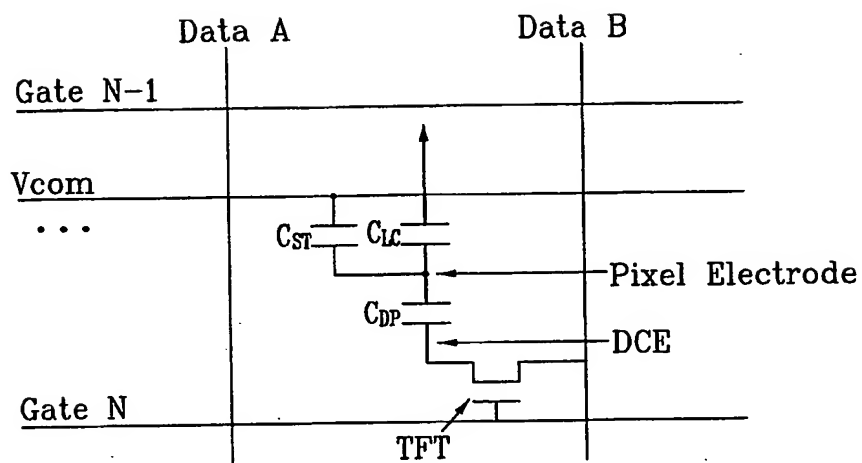
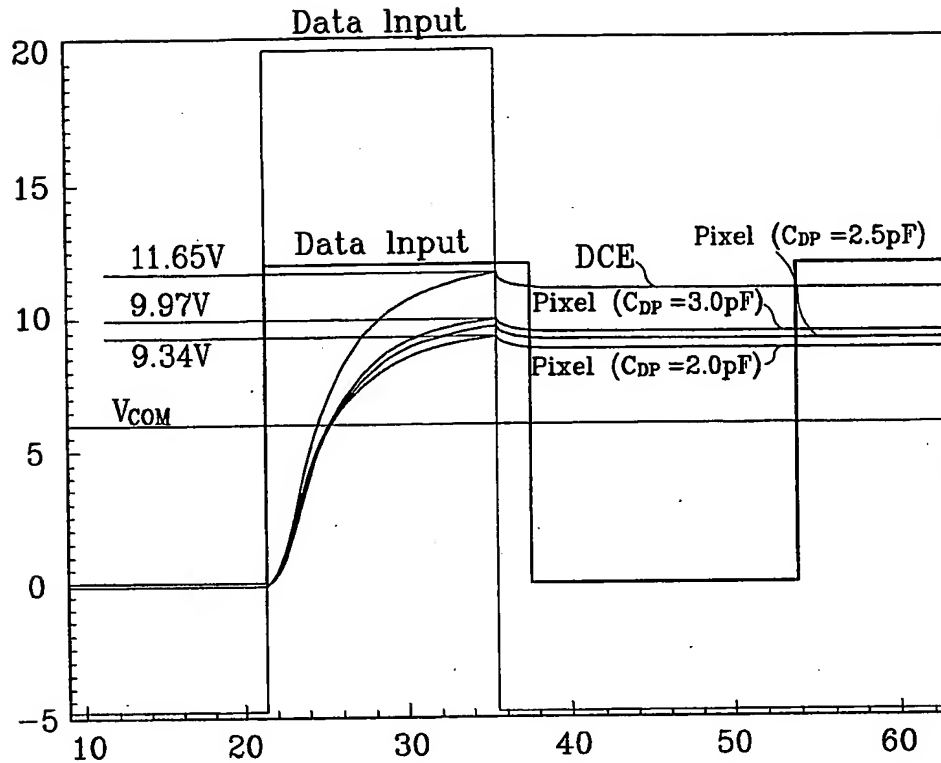


FIG. 1



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FIG. 2



## Simulation Parameter

TFT W/L=30 $\mu$ m/3.5 $\mu$ m,C<sub>DCE\_PIXEL</sub>=2.0pF, 2.5pF, 3.0pF (Split)=C<sub>DP</sub>C<sub>DCE\_COM</sub>=0.01pFC<sub>ST</sub>=0.02pFC<sub>LC</sub>=0.3pFR<sub>LC</sub>=30 $\Omega$ 

Gate &amp; Data R,C Load=17" With reference to center

FIG. 3A

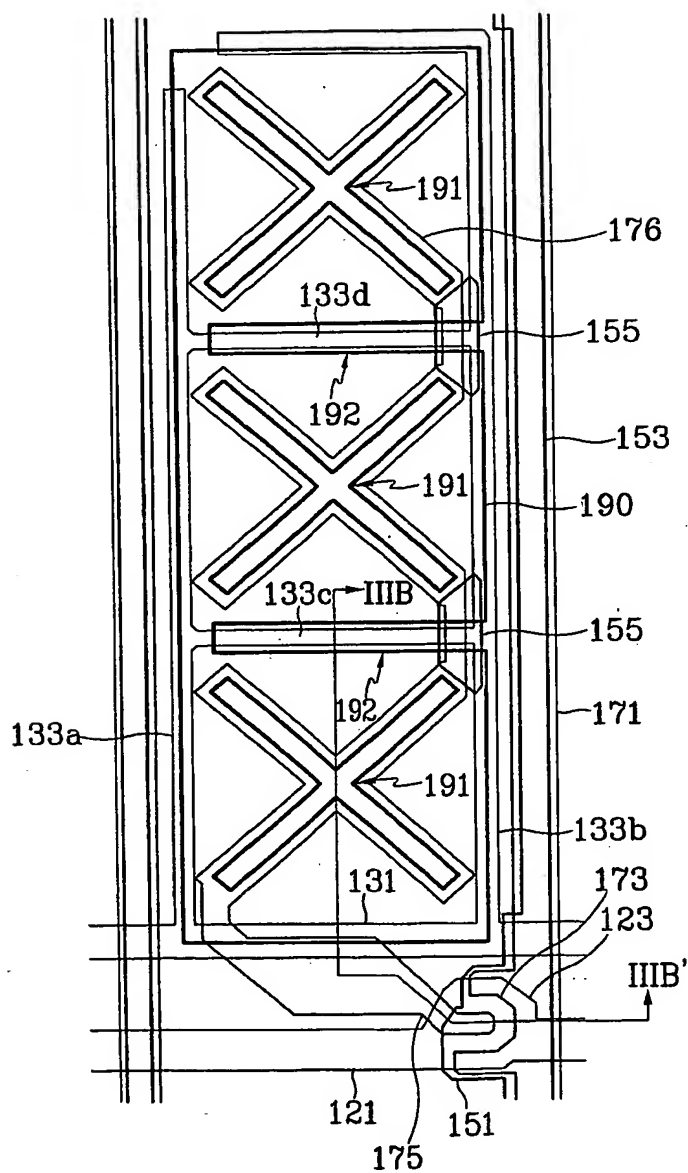
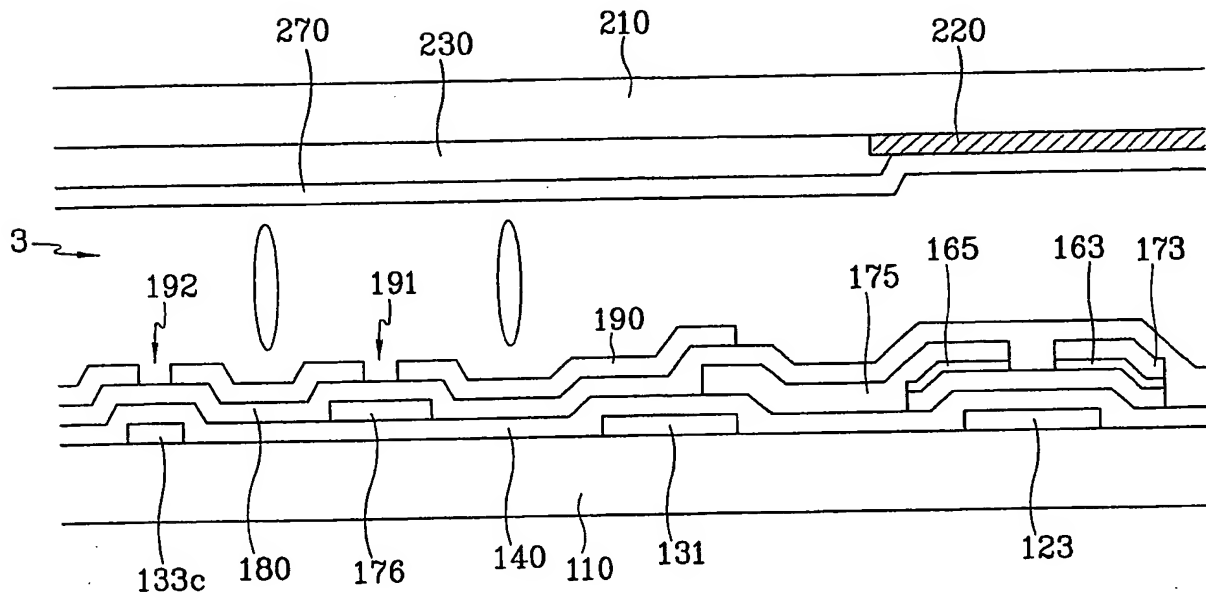
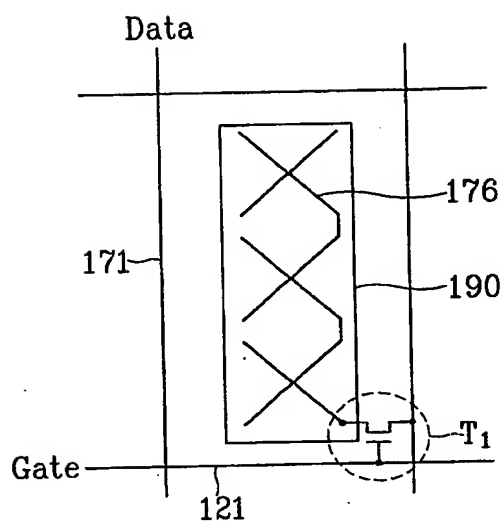


FIG. 3B



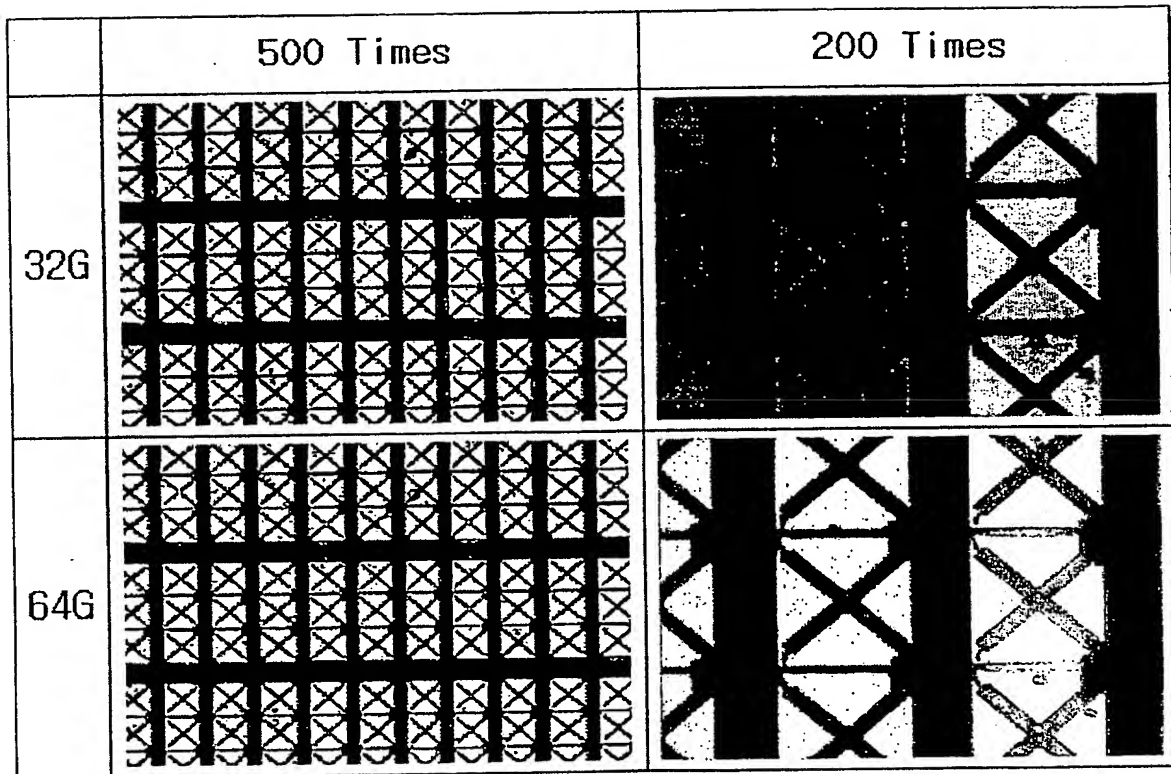
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FIG. 4



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FIG.5



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FIG. 6A

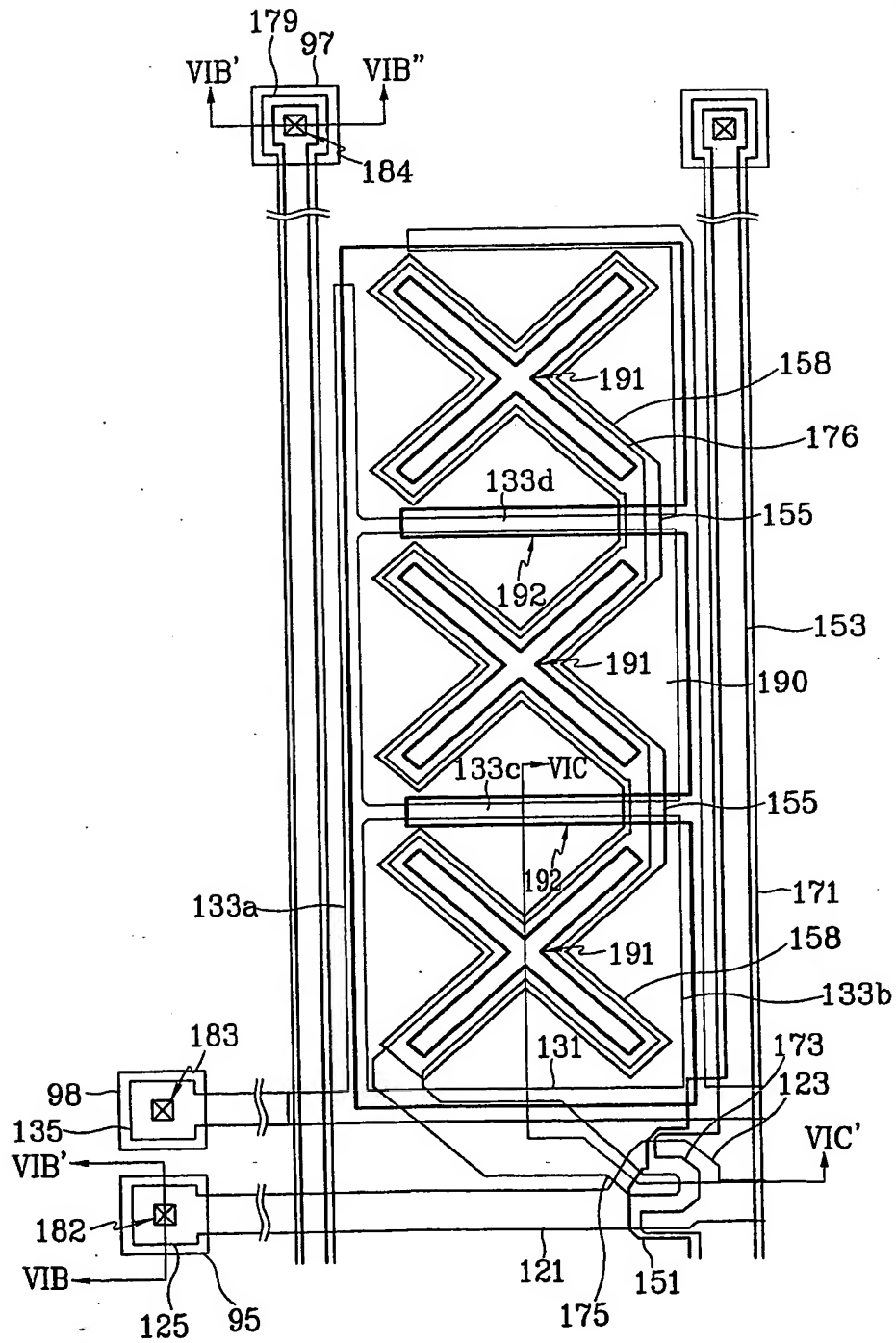


FIG. 6B

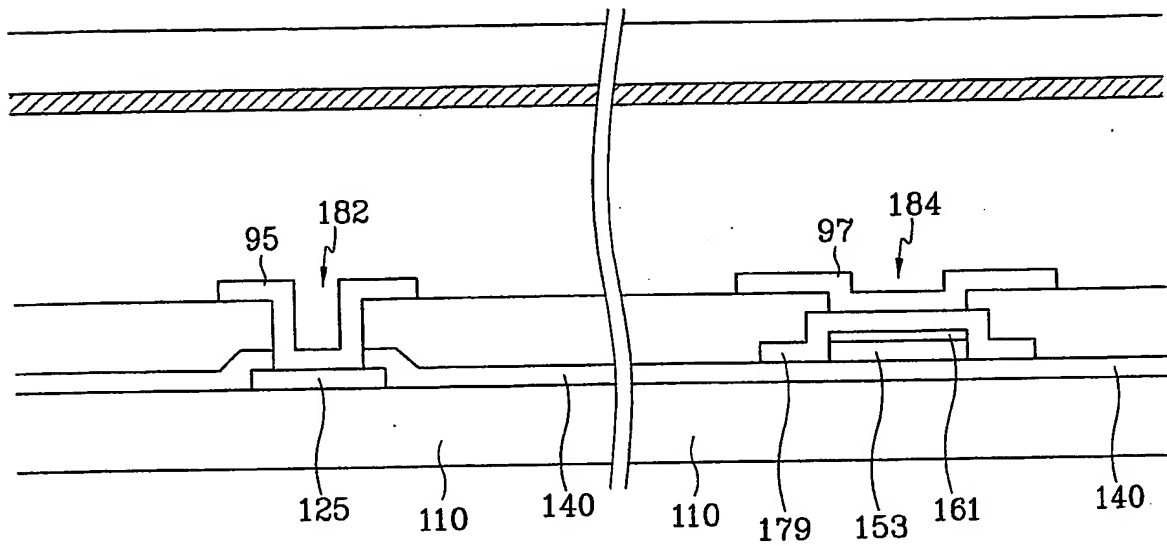
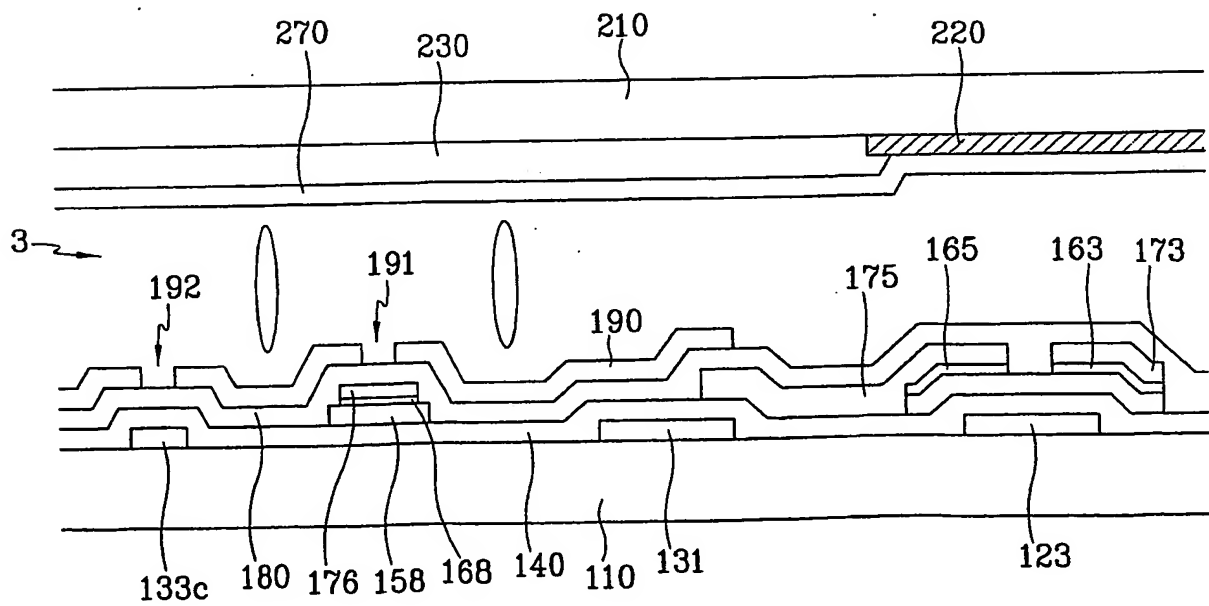


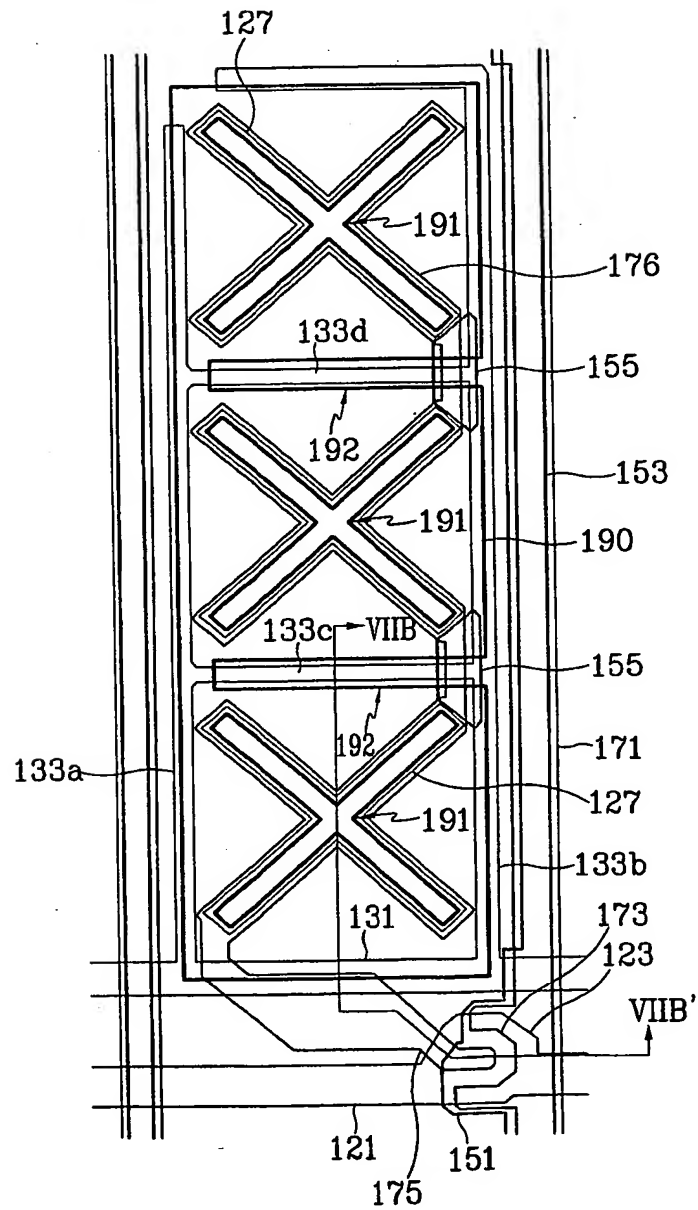
FIG. 6C





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FIG. 7A



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FIG. 7B

